

Please amend the claims as follows:

1. (original) A sigma-delta modulator, comprising:
 - at least two parallel filters, each receiving an input signal
 - a gain device for controlling a weight associated with an output of each of the at least two parallel filters, and
 - a quantizer for quantizing a weighted output from said gain device.
2. (original) The sigma-delta modulator of Claim 1, wherein an output of said quantizer is fed back as an input to said at least two parallel filters.
3. (currently amended) The sigma-delta modulator of ~~Claims 1-~~
2Claim 1, wherein at least one of said at least two parallel filters is a high order filter and at least one of said at least two parallel filters is a low order filter.
4. (original) A method of sigma-delta modulation, comprising:
 - inputting a signal to at least two parallel filters,

controlling a weight associated with an output of each of the at least two parallel filters, and

quantizing a weighted output from the at least two parallel filters.

5. (original) The method of Claim 4, wherein an output of said quantizing is fed back as an input to the at least two parallel filters.

6. (currently amended) The method of ~~Claims 4 or 5~~Claim 4, wherein at least one of the at least two parallel filters is a high order filter and at least one of the at least two parallel filters is a low order filter.

7. (currently amended) A signal processing apparatus comprising:
an input for obtaining an input signal,
a sigma-delta modulator as claimed ~~in any of the claims~~
~~1-3 for obtaining an output signal~~Claim 1, and
an output unit for providing said output signal.